Adder

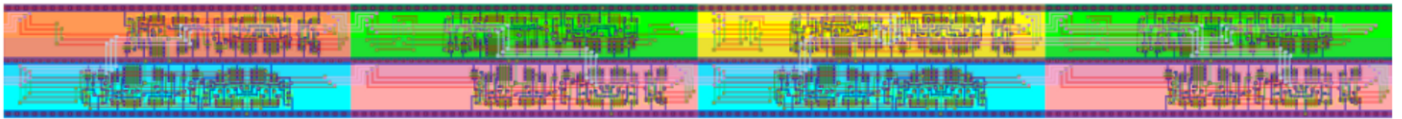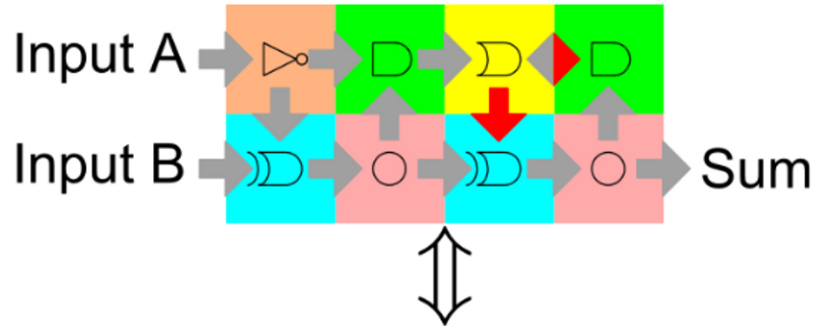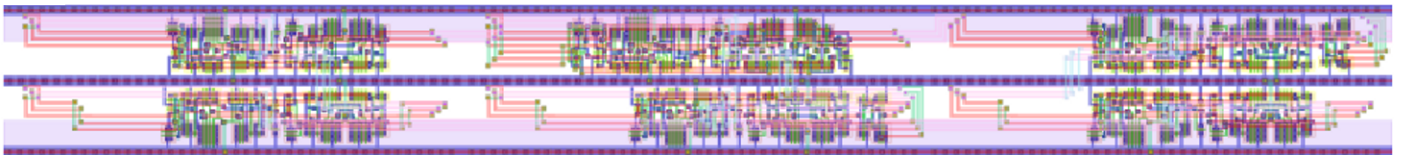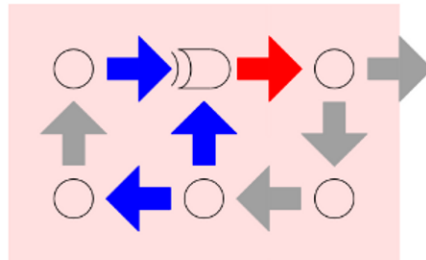**3 Bit Linear Feedback Shift Register**

image: Forrest Green © 2011

Equivalence between an Asynchronous Logic Automata (ALA) design and its implementation in an integrated circuit.